



# Deep Learning Inference on Embedded Devices: Fixed-Point vs Posit

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# Introduction

## Why Deep Learning Inference on Embedded Devices?

Most digital neuromorphic chips are specialized for data centers

Drawbacks of performing Deep Learning inference in data centers:

- ❖ Latency
- ❖ Accessibility
- ❖ Security



[3]



[2]



[1]

# Introduction

What are the challenges in designing/performing Deep Learning architectures for embedded devices ?

- ❖ real time performance
- ❖ **Energy consumption**

Solutions for energy reduction ?

- ❖ Low precision Arithmetic
  - Fixed-point number system

$\pi$

3.14159 26535

[4]

$\pi$

3.14

[4]

# Introduction

Current Approaches

- ❖ Most previous work requires
  - Quantization techniques
  - Retraining

What is missing ?

- ❖ Fixed-point number system represents numbers **uniformly**
- ❖ The parameters are distributed **non-uniformly**

What is the solution ?

- ❖ Using Posit Number System [5,6]
  - **Tapered number system** [7]
  - (non-uniform distribution)
  - More accurate than floating point

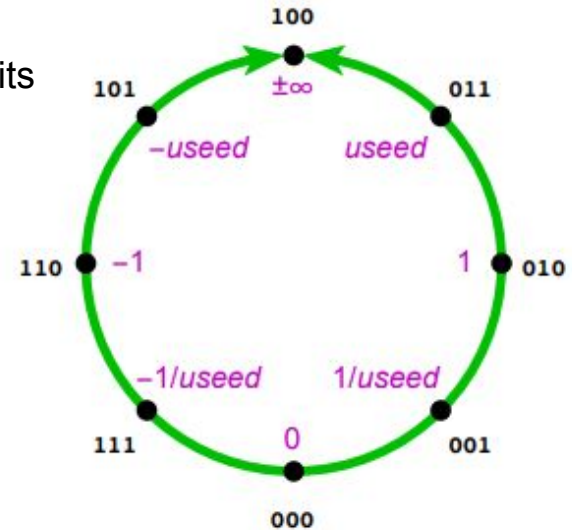
# Posit Number System

- ❖ Proposed by John L. Gustafson, 2017 [5,6]
- ❖ Define :  $p(n, es)$  n= number of bits  
es= number of exponent bits

$$useed = 2^{2^{es}}$$

$$min = useed^{-n+2}$$

$$max = useed^{n-2}$$



[6]

$$X = (-1)^{sign} \times (useed)^{r_{value}} \times 2^{exponent} \times (1 + fraction)$$

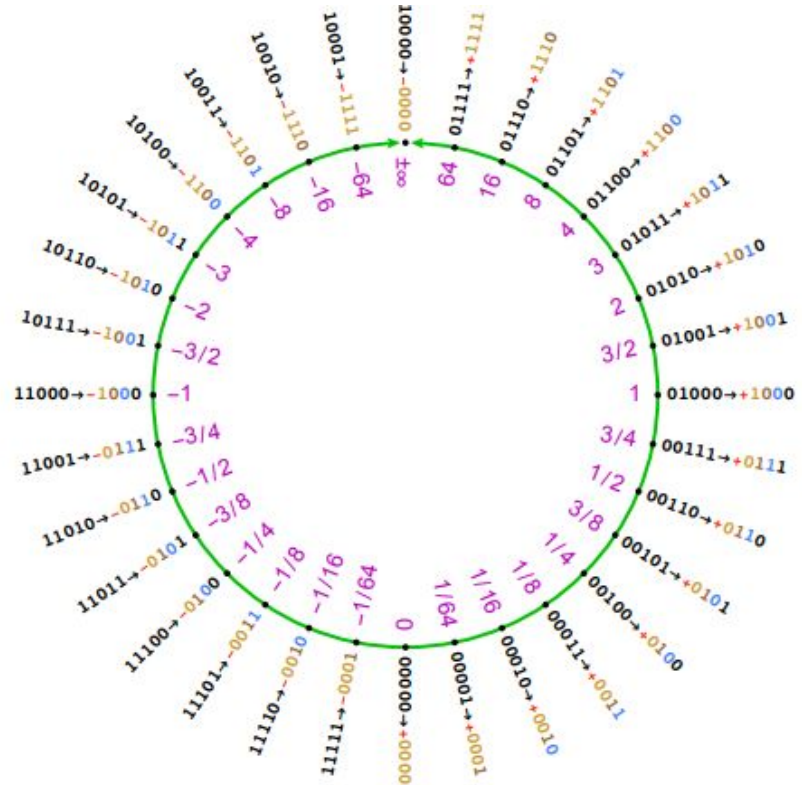
# Posit Number System

Example:  $P_{(5,1)}$

$useed = 4$

$max = 64$

$min = 1/64$



[6]

# Posit Number System

Conversion from Posit to real number

- ❖ Sign
- ❖  $r_{value}$  → Leading zero detection,  
Leading one detection
- ❖ Exponent value
- ❖ Fraction value

Conversion from real number to Posit

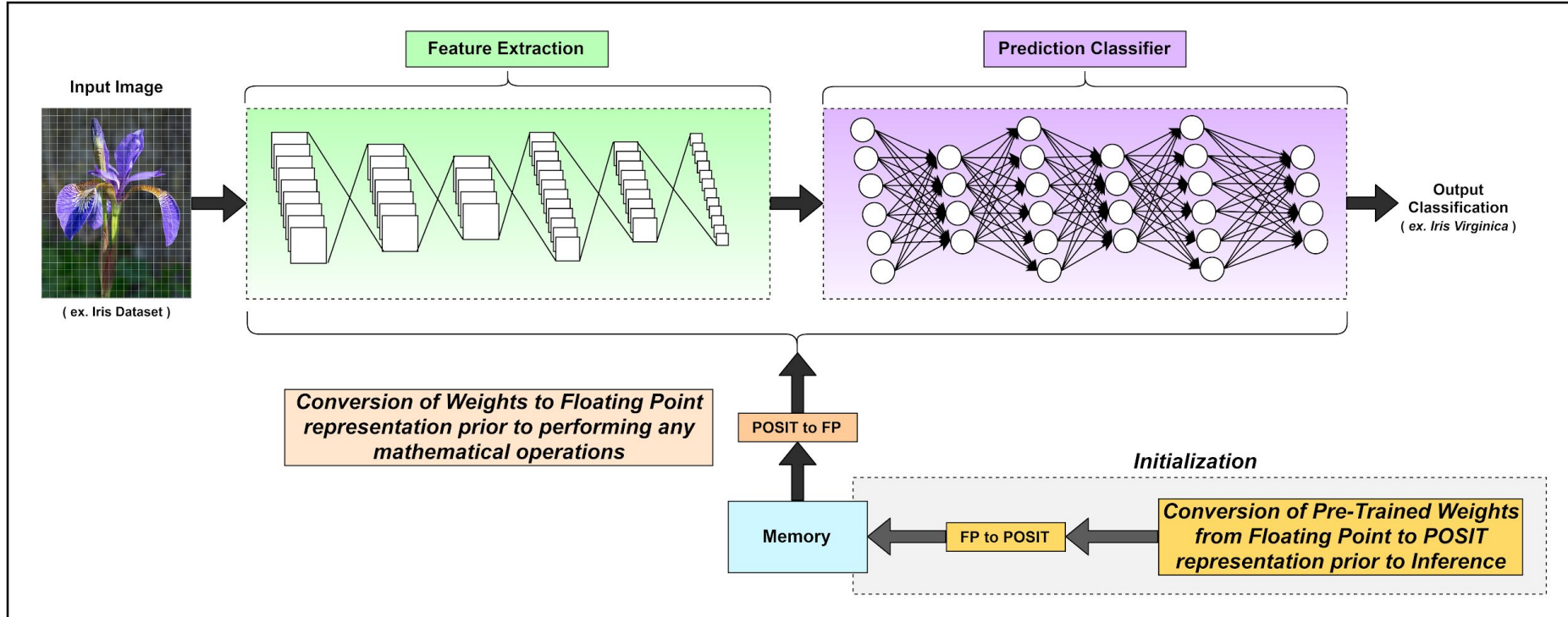
- ❖  $r_{value}$  = Divide or multiply by 2 → [1,used)
- ❖ exponent = Divide or multiply by 2 → [1,2)
- ❖ Fraction = rest of bits

$$X_p = \begin{array}{|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|} \hline s & r & r' & e & f_{11} & f_{10} & f_9 & f_8 & f_7 & f_6 & f_5 & f_4 & f_3 & f_2 & f_1 & f_0 \\ \hline \end{array}$$

$$X_b = \begin{array}{|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|} \hline 0 & 1 & 0 & 1 & 0 & 1 & 0 & 0 & 0 & 1 & 1 & 1 & 1 & 0 & 1 & 1 \\ \hline \end{array}$$

$$X_d = 4^0 \times 2^1 \times (1 + 0.280) = 2.56$$

# Proposed Architecture





# Experiments

## Task:

- ❖ Handwritten numeral classification, Image classification

## Parameters:

- ❖ Weights

## Number Systems :

- ❖ Single Precision Floating Point Number System
- ❖ Variable Length Fixed-point Number System
  - Integer part = 1 bit
  - Fraction part = [0,15]
- ❖ Normalized Posit Number System ,  $p_{(i,0)}$  where  $i = [2,8]$

## Deep Neural Networks:

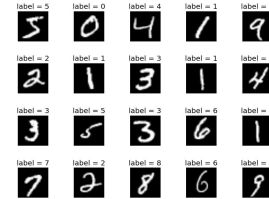
- ❖ LeNet-4 (2 Conv, 2 FC), ConvNet (3 Conv, 2 FC), AlexNet (5 Conv, 3 FC)

## Metric:

- ❖ Top-1 accuracy, memory utilization, memory access

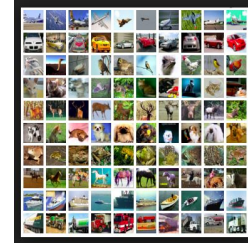
# Datasets

- ❖ MNIST Dataset [9]
  - Categories = 10
  - Inference = 10000



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- ❖ Cifar-10 Dataset [11]
  - Categories = 10
  - Inference = 10000



[10]

- ❖ Subset of ImageNet [13]
  - Categories = 10
  - Inference = 10000



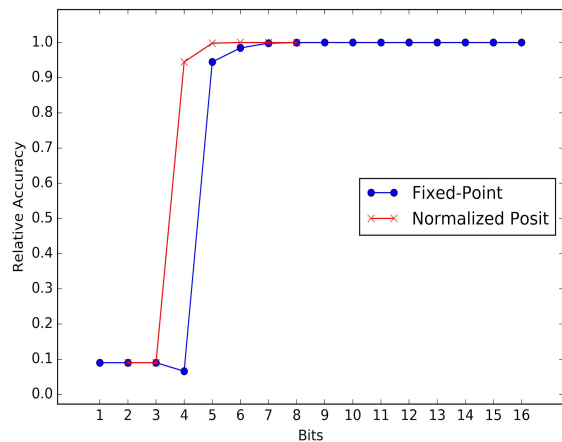
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# Baseline Results

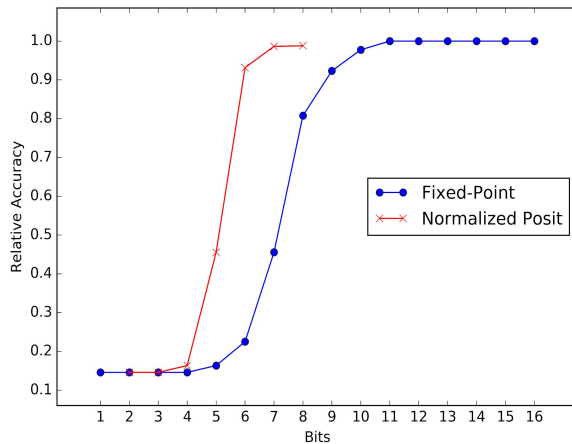
**Single Precision Floating Point Number System:**

Task	Dataset	# inference set	Network	Layer	Top-1 Accuracy
Digit Classification	MNIST	10000	Lenet	2 Conv and 2 FC	<b>99.03%</b>
Image Classification	CIFAR10	10000	Convnet	3 Conv and 2 FC	<b>68.45%</b>
Image Classification	ImageNet	10000	AlexNet	5 Conv and 3 FC	<b>55.45%</b>

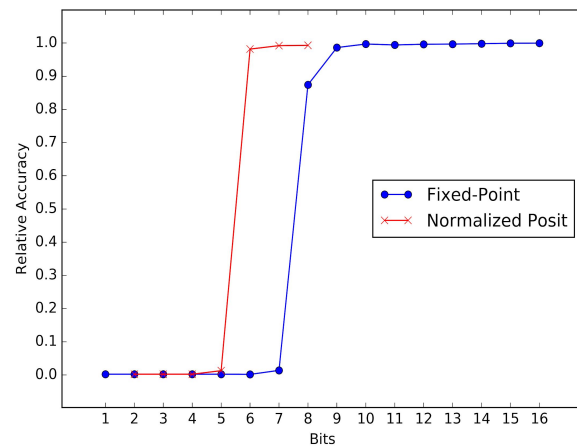
# Results for proposed architecture on different datasets



**MNIST**



**CIFAR-10**



**ImageNet**

# Summary of Results

Dataset	Network	# bits ( FP )	# bits (FIX) 1% accuracy degradation	# bits ( NP ) 1% accuracy degradation	Memory utilization
MNIST	Lenet	32	7	5	<b>28.6%</b>
CIFAR10	Convnet	32	11	7	<b>36.4%</b>
ImageNet	AlexNet	32	9	7	<b>23%</b>

FP = Floating Point

FIX = Fixed-point

NP = Normalized posit

- It can also reduce the number of memory accesses through memory concatenation schemes.

# Conclusions

## ❖ Exploring the use of Posit Number System in DNNs

- Weights
- 3 DCNNs and 3 Datasets
- Posit outperformed the fixed-point implementations in terms of accuracy and memory utilization
- We estimate that the use of Posit can help reduce the number of memory accesses

## ❖ Future work

- Hardware implementation
- Consideration of conversion overheads
- Using the Posit number system for activation
- Posit number system for other deep neural networks and Training Deep Learning Networks

# Questions



# References

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